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Attorney's Docket No. 05655.P001

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:)	
David A. Carlson and Vishnu V. Yalala)	Art Unit: Not yet assigned
)	
Application No: Filed concurrently herewith)	Examiner: Not yet assigned
)	
Filed: February 6, 2002)	
)	
For: METHOD AND APPARATUS FOR)	
MONTGOMERY MULTIPLICATION)	

BOX PATENT APPLICATION
Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. § 1.97

Sir:

Enclosed is a copy of Information Disclosure Form PTO-1449B together with copies of the cited documents. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449B be initialed by the Examiner to indicate such consideration, and a copy thereof returned to Applicants.

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Applicants do not believe any fee is due with this submission, however, should a fee be required, please charge Blakely, Sokoloff, Taylor, & Zafman LLP Deposit Account No. 02-2666.

Respectfully submitted,
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

A handwritten signature in black ink, appearing to read "Clive D. Menezes".

Dated: February 6, 2002

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	Filed concurrently herewith
		Filing Date	February 6, 2002
		First Named Inventor	David A. Carlson
		Group Art Unit	Not yet assigned
		Examiner Name	Not yet assigned
		Attorney Docket Number	05655.P001
Sheet	1	of	2

10/068677
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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		Blum, T. and Paar, C., "High Radix Montgomery Modular Exponentiation on Reconfigurable Hardware", ECE Department, Worcester Polytechnic Institute, pages 1-13	
		Blum, Thomas, "Modular Exponentiation on Reconfigurable Hardware", Thesis Submitted to the Faculty of Worcester Polytechnic Institute, April 8, 1999, 113 pages	
		Elbirt, AJ and Paar, C., "Towards an FPGA Architecture Optimized for Public-Key Algorithms", Presented at the SPIE:s Symposium on Voice, Video, and Comm., 9/20/99, pages 1-10	
		Kim, Chinuk, "VHDL Implementation of Systolic Modular Multiplications on RSA Cryptosystem", Thesis at The City College of the City University of New York, Jan. 2001, 43 pages	
		Gutub, Adnan, "A Modulo Multiplication Hardware Design", Project Report at Oregon State University, Electrical & Computer Engineering Department, Winter 2000, 8 pages	
		Poldre, J. et al., "Modular Exponent realization on FPGAs", Tallinn Technical University, Computer Engineering Department, 12 pages	
		Savas, E. et al., "A Scalable and Unified Multiplier Architecture for Finite Fields GF(p) and GF(2m)", Oregon State University, Electrical and Computer Engineering, 20 pages	
		Shand, M. et al., "Fast Implementation of RSA Cryptography", Digital Equipment Corp., Paris Research Laboratory, 9 pages	
		Tenca, Alexandre F. and Koc, Cetin K., "A Scalable Architecture For Montgomery Multiplication", Oregon State University, Electrical and Computer Engineering Department, 13 pages	
		"How SSL Works", http://developer.netscape.com/tech/security/ssl/howitworks.html , 6/1/01	
		Savolainen, Sampo, "Internet Key Exchange (IKE)", Helsinki University of Technology, Department of Electrical and Communications Engineering, November 22, 1999, http://www.niksula.cs.hut.fi/sjsavola/SoN/essay.html , 12 pages	

Examiner Signature	Date Considered
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Sheet	2	of	2		

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